

FORM PTO-1449 (SUBSTITUTE)		Attorney Docket No.: W&B-INF-1960 Appl. No.:	
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))		Applicant: ROBERT KAISER ET AL.	
		Filing Date: October 20, 2003 Group Art Unit:	

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
<i>BG</i>	A	6,115,828	9/5/00	Tsutsumi et al.			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J						
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

<i>BB</i>	Lucente, M. A. et al.: "Memory System Reliability Improvement Through Associative Cache Redundancy", IEEE Journal of Solid State Circuits, Vol. 26, No. 3, March 1991, pages 404-409

EXAMINER	<i>[Handwritten signature]</i>	DATE CONSIDERED	<i>4-7-06</i>